

Serial No.: 10/092,690

Attorney Docket No.: 02P03755US

REMARKS

Upon entry of the instant Amendment, Claims 1-15 are pending. Applicants gratefully acknowledge that claims 2-10 and 12-14 were indicated to be allowable.

Claims 1, 11, and 15 were rejected under 35 U.S.C. 103 as being unpatentable over Mochizuki, U.S. Patent No. 6,580,901 ("Mochizuki") in view of Lomp, U.S. Patent Publication No. 20020118653 ("Lomp"). Applicants respectfully submit that the claimed invention is not taught, suggested, or implied by Mochizuki or Lomp, either singly or in combination.

As discussed in the Specification, embodiments of the present invention relate to a power control system using an open loop technique for relatively low power levels and a closed loop technique at higher power levels.

In the open loop technique, the wireless telephone stores a phasing table of automatic power control (APC) values for the power levels (which are used to control upconverter gain levels measured at different channels. In operation, a power controller reads the power level and reads the APC value in the table. Another table stores the APC value for one power level as the channel and temperature are varied. This value is interpolated, as needed, during operation. The APC value for the open loop approach is determined by reading the input channel; finding the closest higher and closest lower channels in the temperature-channel table; interpolating between APC values for the temperature column of the closest lower temperature, to get the actual value. The difference between this value and the APC value for the actual power level in the phasing table is then obtained.

In the closed loop approach, the actual power output is read from a power detector and the APC value is adjusted until the output of the power detector corresponds to the value which gives the required power level. A phasing table of power levels and power detector values is used. To factory calibrate these values, the APC value is adjusted until the nominal power for each power level is output and the power detector value is stored. A two pole IIR (Infinite Impulse Response) filter may be used to filter the power detector output.

In operation, in certain embodiments, the closed loop control algorithm runs every

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transmit burst and reads the power detector when the power is off; reads the power detector when the transmitter is on and subtracts these two numbers to obtain the actual RF power level; from the power level, looks up the desired RF power value in the phasing table to find an RF power error; and runs a servo control loop calculation to find the APC value needed to correct the error.

Thus, claim 1 has been amended to recite "said first mode comprising a low power mode, said second mode comprising a high power mode;" claim 11 has been amended to recite "wherein said open loop power controller is adapted to provide a power set (APC) value in a first low power mode and said closed loop power controller is adapted to provide said power set value in a second high power mode;" and claim 15 has been amended to recite an open loop power controller adapted to provide a automatic power control (APC) value in a low power mode and based on a channel-temperature table; a closed loop power controller adapted to provide an APC value in a high power mode."

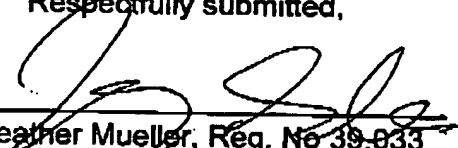
In contrast, neither Mochizuki nor Lomp appear to have anything at all to do with, inter alia, a channel-temperature table associated with an open loop mode as generally recited in the claims at issue. While Mochizuki provides a control unit 8 that provides a control to an amplifier, Mochizuki provides no indication at all that this has anything to do with a temperature-channel table. Lomp relates to a closed loop technique and thus, like Mochizuki, has nothing to do with providing, inter alia, a channel-temperature table, as generally recited in the claims at issue. As such, the Examiner is respectfully requested to reconsider and withdraw the rejection.

For all of the above reasons, Applicants respectfully submit that the application is in condition for allowance, which allowance is earnestly solicited.

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Respectfully submitted,

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